

YO999-153DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re divisional patent application of 09/325,732 filed on June 4, 1999

Chen, et al.

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: METHOD FOR FABRICATING COMPLEMENTARY METAL OXIDE  
SEMICONDUCTOR (CMOS) DEVICES ON A MIXED BULK AND SILICON-ON-  
INSULATOR (SOI) SUBSTRATE

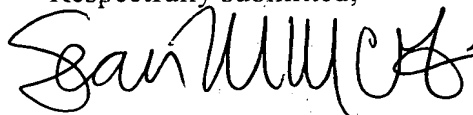
Assistant Commissioner of Patents  
Washington, D.C. 20231

SUBMISSION OF PROPOSED DRAWING CORRECTIONS

Sir:

Submitted herewith is a corrected formal drawing for Figure 6 which has been approved in the parent application. Please substitute the attached drawing Figure for the original drawing Figure which was filed with the application.

Respectfully submitted,



Sean M. McGinn  
Registration No. 34,386

Date: 12/27/02  
McGinn & Gibb, PLLC  
Intellectual Property Law  
8321 Old Courthouse Road, Suite 200  
Vienna, Virginia 22182-3817  
(703) 761-4100  
Customer No. 21254

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Letter to  
Draftsman  
RABCO  
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